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NOV 17 2005 W

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

S. MIYAZAKI et al

Application No.:

10/505,216

Filed:

August 20, 2004

For:

SEMICONDUCTOR INTEGRATED CIRCUIT

Art Unit:

2827

Examiner:

T. LE

UNDER 37 CFR §§ 1.97 & 1.98

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 November 17, 2005

Sir:

In the matter of the above-identified application, applicants are submitting herewith a copy of a communication from a foreign patent office in a counterpart foreign application and copies of the documents listed in the attached form PTO/SB/08A for the Examiner's consideration.

This Information Disclosure Statement is being submitted on information and belief, before the mailing date of either a final action or a notice of allowance and is accompanied by the following certification specified in 37 CFR §1.97(e):

"On information and belief, I hereby certify that each item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement."

Each of the documents listed on the attached form PTO/SB/08A is in the English language.

It is respectfully requested that this Information Disclosure Statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (Dkt. 1374.43896X00) and, please credit any excess fees to such deposit account.

Respectfully submitted,
ANTONELLI, TERRY, STOUT & KRAUS, LLP

Gregory E. Montone Reg. No. 28,141

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1300 North Seventeenth Street, Suite 1800

Arlington, Virginia 22209 Telephone: (703) 312-6600 Facsimile: (703) 312-6666

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PTO/SB/08A (10-01)
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Substitute	Substitute for form 1449A/PTO			Complete if Known			
·				Application Number	10/505,216		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Filing Date	Aug. 20, 2004		
				First Named Inventor	S. MIYAZAKI et al		
				Art Unit	2827		
(use as many sheets as necessary)				Examiner Name	T. LE		
Sheet	1	of	1	Attorney Docket Number	1374.43896X00		

				U.S. PATENT	DOCUMENTS	
Examiner Initials'	Cite No.1	Document Number Number-Kind Code ² (if known)		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		6.252.813	B1	06-26-2001	Liu	

		FORE	GN PATENT	OCUMENTS		
Examiner	Cite	Foreign Patent Document	Publication	Name of Patentee or	Pages, Columns, Lines,	
Initials'	No.¹	Country Code ³ –Number ⁴ –Kind Code ⁵ (if known)	Date MM-DD-YYYY	Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	T ⁶
		EP 0810606 A1	12-03-1997	SGS-THOMSON MICRO- ELECTRONICS s.r.l.		
		EP 506989 A1	04-02-1991	Kabushiki Kaisha Toshiba		
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OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²	

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard St.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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